

UTILITY PATENT APPLICATION
RCA-89210

Serial No. 09/830,235

## IN THE CLAIMS:

This list replaces all pending claims in the application with the following claims:

1. (currently amended) An apparatus for loading computer code from a memory type integrated circuit card preloaded with computer code, said apparatus comprising:

a card interface capable of distinguishing between a conventional integrated circuit card and a <u>said</u> memory card, <u>said card interface having a low speed data port</u> and a high speed data port;

a memory for storing computer code for execution by the apparatus; and
a memory card comprising a memory unit and a memory unit controller; and
a computer controlled device memory unit for storing a first computer code
that is downloaded from the memory unit of the memory card.

a microcontroller coupled to the card interface and to the memory for, if said card is a memory card, reading said computer code from said memory card by way of said high speed data port to said memory, for thereby updating the computer code stored in said memory so as to effect a change of the functional operation of the apparatus.

- (CANCELED)
   (CANCELED)
- 5. (currently amended) The apparatus of claim 1, wherein said card interface comprises:

means for producing a first signal that is coupled to an integrated circuit card connection; and

means for analyzing a second signal that is produced by a <u>said</u> memory card in response to said first signal.



UTILITY PATENT APPLICATION RCA-89210

Serial No. 09/830,235

- (original) The apparatus of claim 5, wherein said second signal is not produced by integrated circuit cards that are not memory cards.
- 7. (previously presented) The apparatus of claim 5, wherein said card interface applies said first signal to a clock signal connector of said integrated circuit card connection and receives said second signal on a data input/output signal connector of said integrated circuit card connection.
- 8. (CANCELEDY
- 9. (previously presented) The apparatus of claim 1 wherein said card interface further comprises:

means for transferring computer code from said memory card to said computer controlled device memory unit,

10. (currently amended) The apparatus of claim 1, wherein said eard interface microcontroller further comprises:

means for accepting or rejecting the computer code for transference from said memory card to said computer controlled device memory unit.

11. (currently amended) A method of loading computer code in a computer controlled device <u>having a smart card interface for receiving a smart card, said method</u> comprising the steps of:

identifying whether the smart card an integrated circuit card is a memory card containing a memory unit with preloaded computer code, and a memory controller, or a conventional integrated circuit card; and

if said card is a memory card, transferring the computer code in said memory card through a high speed data port of a memory card into said computer controlled device; such that the transferred computer code is stored in a memory so as to effect a change in the functionality of the computer controlled device.

UTILITY PATENT APPLICATION
RCA-80210

Serial No. 09/830,235

12. (previously presented) The method of claim 11 wherein said identifying step further comprises the steps of:

applying a first signal to said memory card;

analyzing a second signal produced by said memory card in response to said first signal to determine if said integrated circuit card is a memory card.

13. (previously presented) The method of claim 12 wherein said transferring step further comprises:

activating an NRSS interface.

- 14. (original)The method of claim 11 further comprises: analyzing a header of said computer code to determine the validity of the computer code.
- 15. (original) The method of claim 11 further comprising toggling a reset signal.
- 16. (currently amended) The method of claim 15, further comprising said memory card monitoring a clock input terminal for said a first signal in response to said toggled reset signal.
- 17. (currently amended) The method of claim 16, wherein said memory card generates said a second signal in response to detection of said first signal.
- 18. (CANCELED).